



Anti-Zapper Board



_____ Installation Guide

International Currency Technologies Corp.

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1. Introduction

1-1. Overview

ICT Anti-Zapper Board transfers RS232(UART) communication interface to Pulse. ICT Anti-Zapper Board products are designed for use under standard conditions to protect ICT bill acceptors listed in this installation guide against the interference generated by certain EMP devices.

Accordingly, ICT shall not be in any way responsible or liable for any damages, losses or expense arising from the use of Anti-Zapper Board under any special or extraordinary environments or conditions.

2. Specifications

Interface	Pulse
Power Source	DC 12V / DC 5V
Power Consumption	200mA
Support	ICT Bill Acceptor
Dimensions	55 x 85 x 25 mm
Environmental	Operating temperature: 0°C~ 60°C Storage temperature: -30°C~70°C Humidity: 30%~ 95%RH (no condensation)

3. Packing List

Main	Anti-Zapper Board
Accessory	Harnesses: Refer to 4-1 Anti-Zapper Board Installation Guide/ DIP Switch Setting Guide

4. Installation

4-1. Harness Application

4-1 TABLE.01

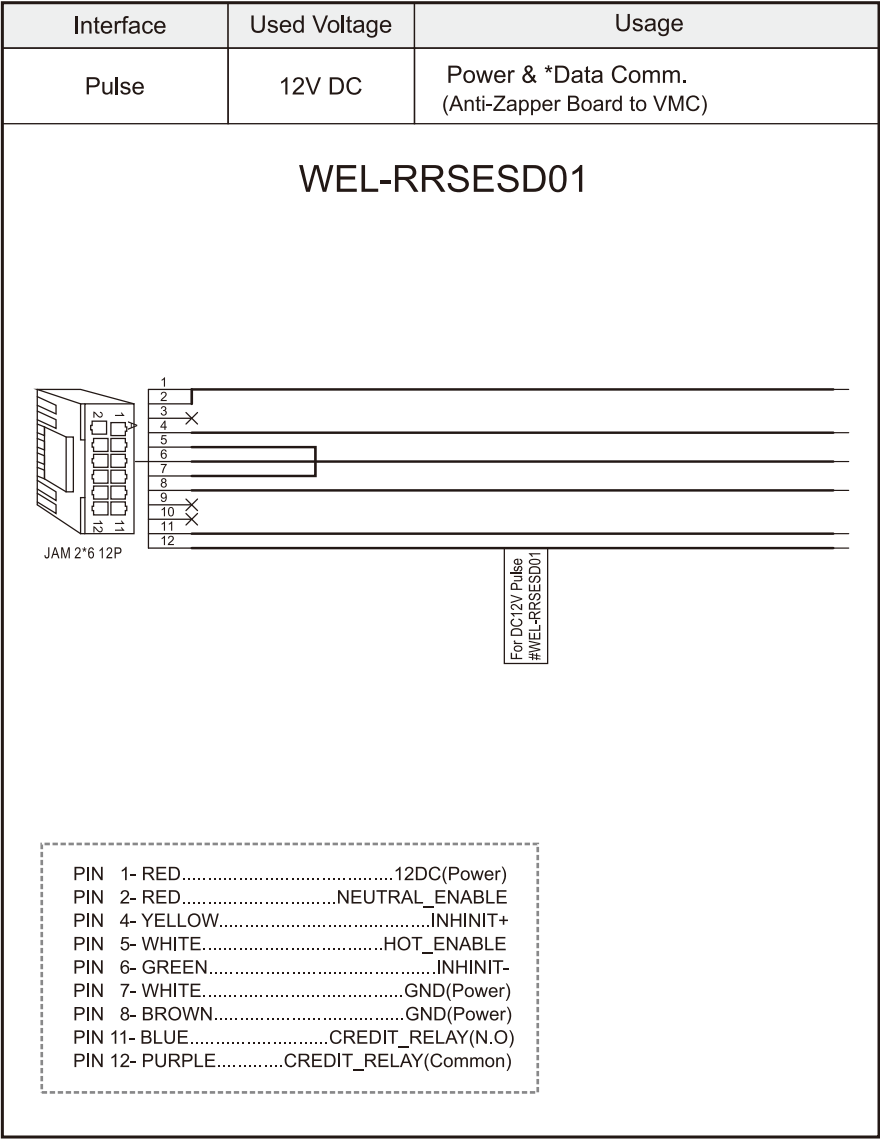
Model Name	Interface	Used Voltage	Usage	Harness	Page
A6, PA7, TAO-A I	Pulse	12V DC	Power & *Data Comm. (Anti-Zapper Board to VMC)	WEL-RRSESD01	5
			Power & *Data Comm. (Anti-Zapper Board to Bill Acceptor)	WEL-RRSESD02	6
A6, PA7, TAO-A I	Pulse	12V DC	Power & *Data Comm. (Anti-Zapper Board to VMC)	WEL-RRSESD19	13
			Power & *Data Comm. (Anti-Zapper Board to Bill Acceptor)	WEL-RRSESD02	6
A6, PA7, TAO-A I	Pulse	117V AC	Power & *Data Comm. (Anti-Zapper Board to VMC)	WEL-RRSESD03	7
		5V DC	Power & *Data Comm. (Anti-Zapper Board to Bill Acceptor)	WEL-RRSESD04	8
		117V AC	Power (Bill Acceptor to VMC)	WEL-RRSESD05	9
A6, PA7, TAO-A I	Pulse	117V AC	Power & *Data Comm. (Bill Acceptor to Anti-Zapper Board, Anti-Zapper Board to VMC)	WEL-RRSESD12	12
		5V DC	Power & *Data Comm. (Anti-Zapper Board to Bill Acceptor)	WEL-RRSESD04	8

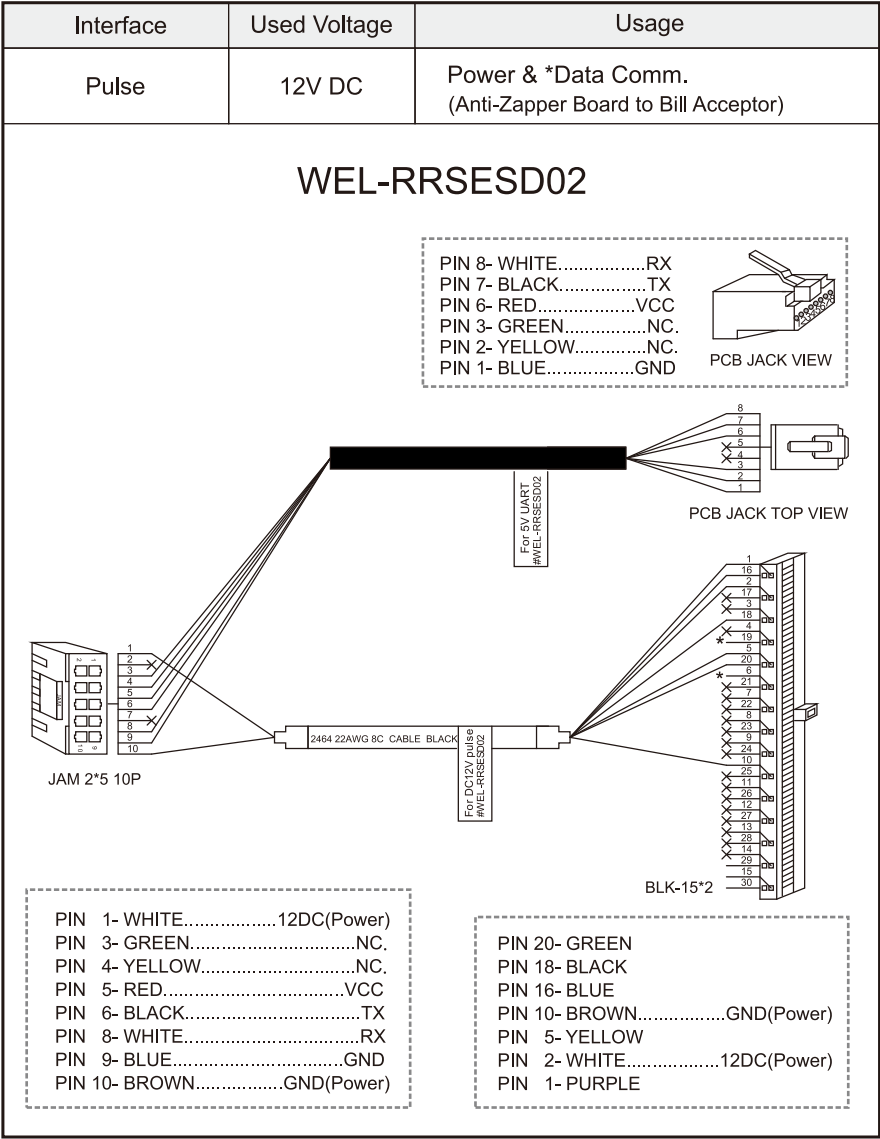
4-1 TABLE.02

Model Name	Interface	Used Voltage	Usage	Harness	Page
P77, L70, L77F	Pulse	12V DC	Power & *Data Comm. (Anti-Zapper Board to VMC)	WEL-RRSESD01	5
			Power & *Data Comm. (Anti-Zapper Board to Bill Acceptor)	WEL-RRSESD06	10
LX7	Pulse	12V DC	Power & *Data Comm. (Anti-Zapper Board to VMC)	WEL-RRSESD01	5
			Power & *Data Comm. (Anti-Zapper Board to Bill Acceptor)	WEL-RRSESD07	11
LX7	Pulse	12V DC	Power & *Data Comm. (Anti-Zapper Board to VMC)	WEL-RRSESD19	13
			Power & *Data Comm. (Anti-Zapper Board to Bill Acceptor)	WEL-RRSESD07	11
LX7-A	Pulse	117V DC	Power & *Data Comm. (Anti-Zapper Board to VMC)	WEL-RRSESD20	14
		5V DC	Power & *Data Comm. (Anti-Zapper Board to Bill Acceptor)	WEL-RRSESD04	8
LX7-A	Pulse	117V DC	Power & *Data Comm. (Anti-Zapper Board to VMC)	WEL-RRSESD03	7
		5V DC	Power & *Data Comm. (Anti-Zapper Board to Bill Acceptor)	WEL-RRSESD04	8
		117V DC	Power (Bill Acceptor to VMC)	WEL-RRSESD21	15

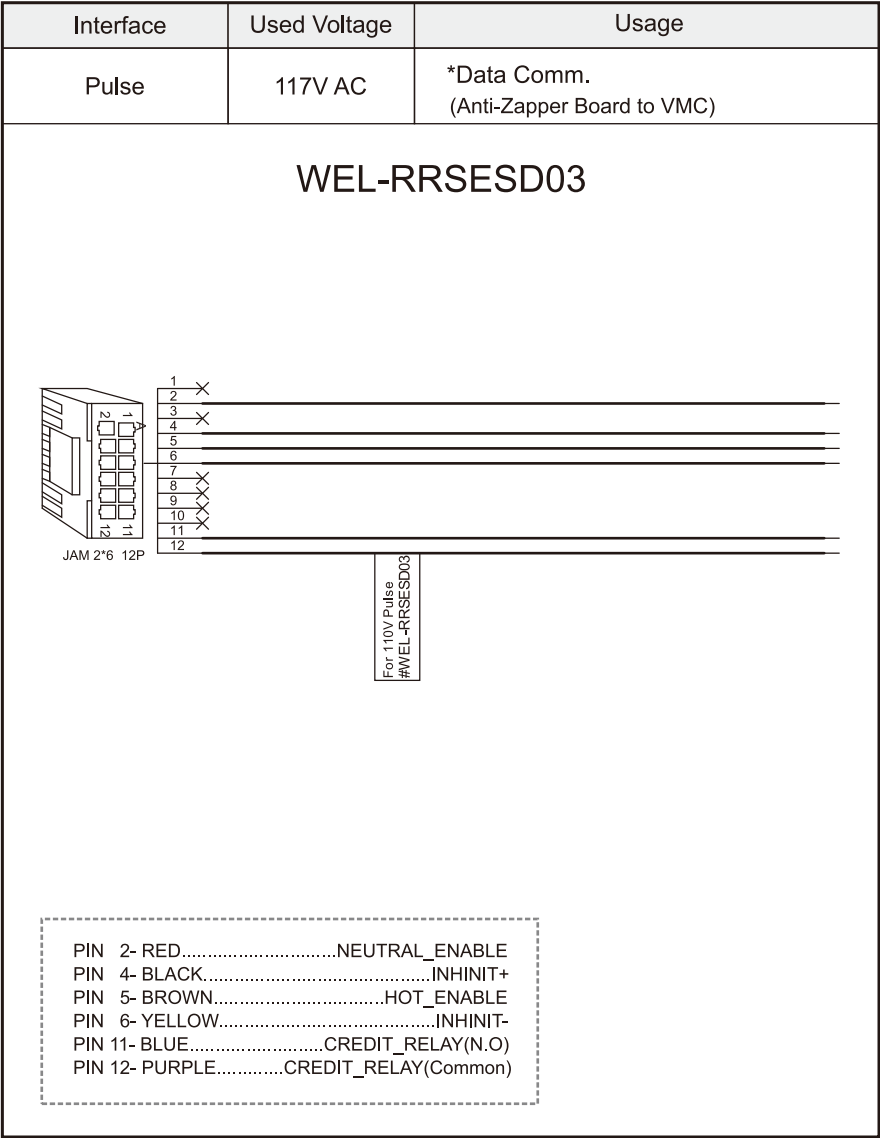
*Data Comm. : Data Communication.

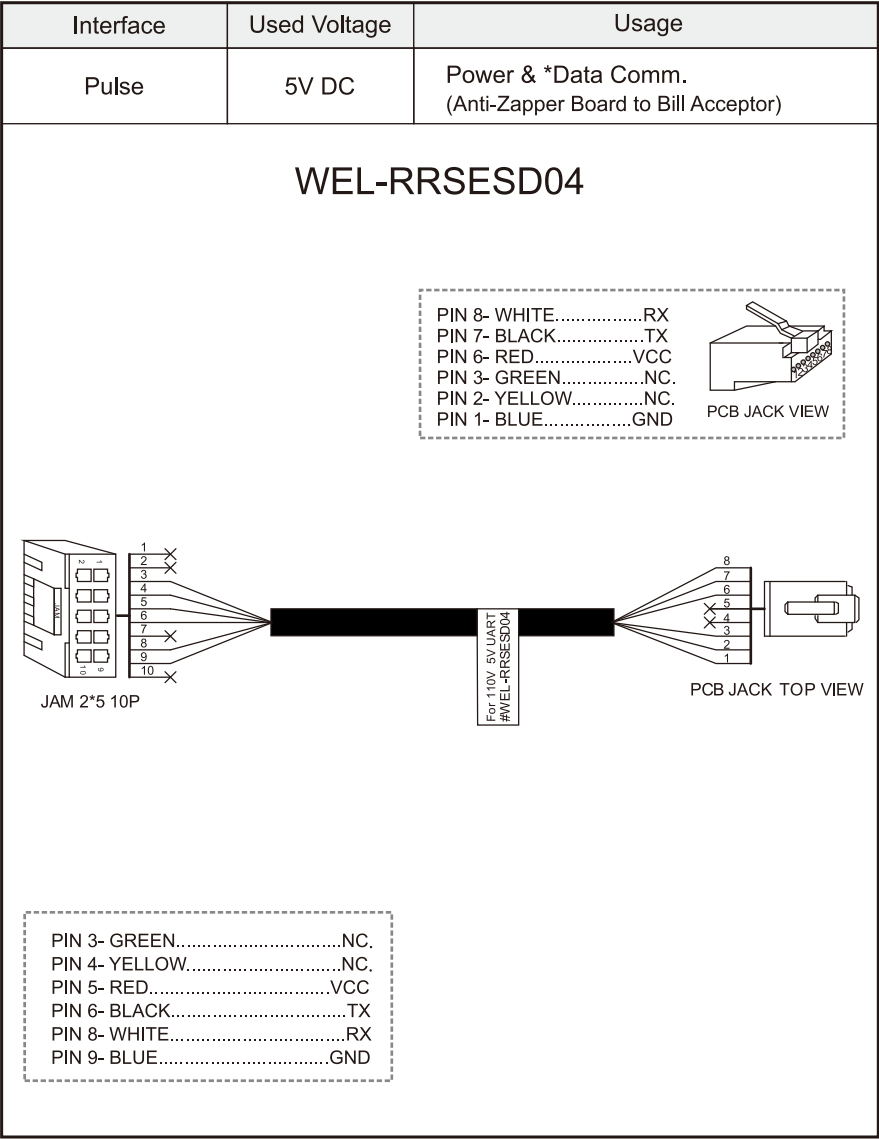
4-1 FIG.01



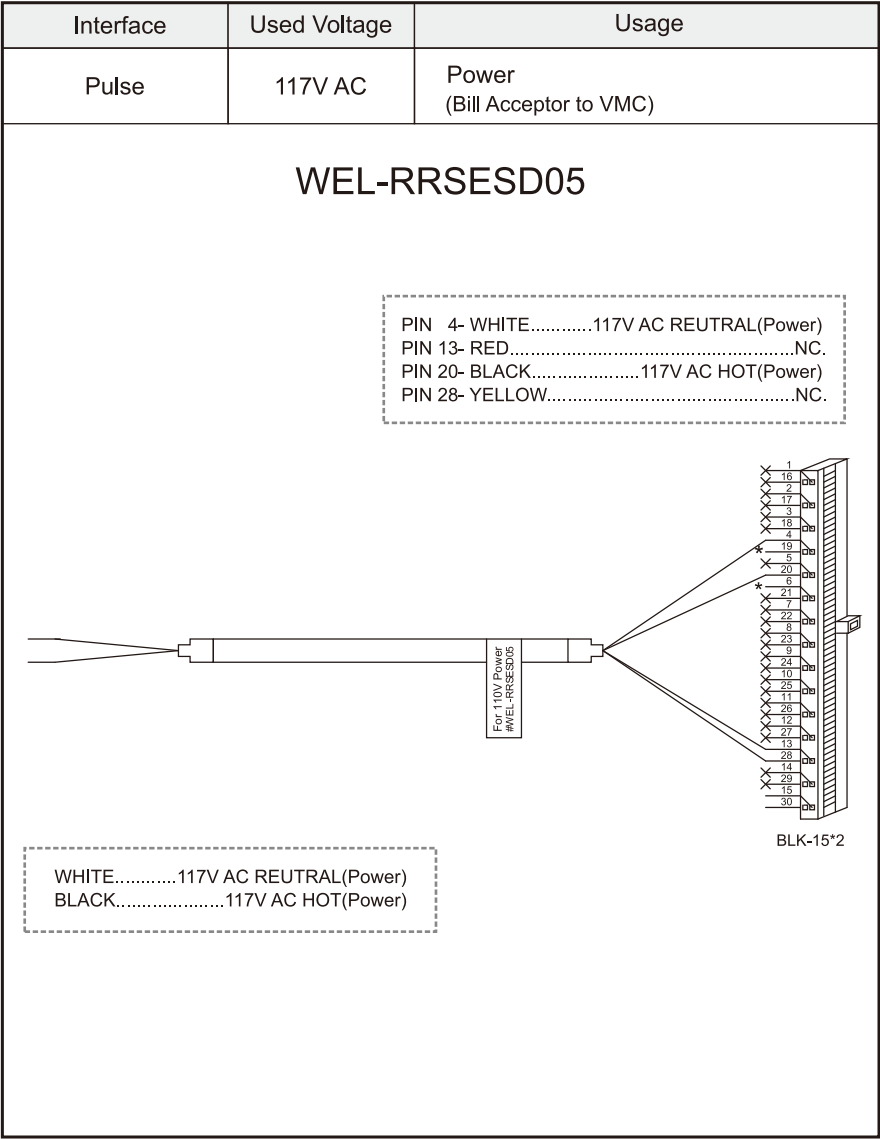


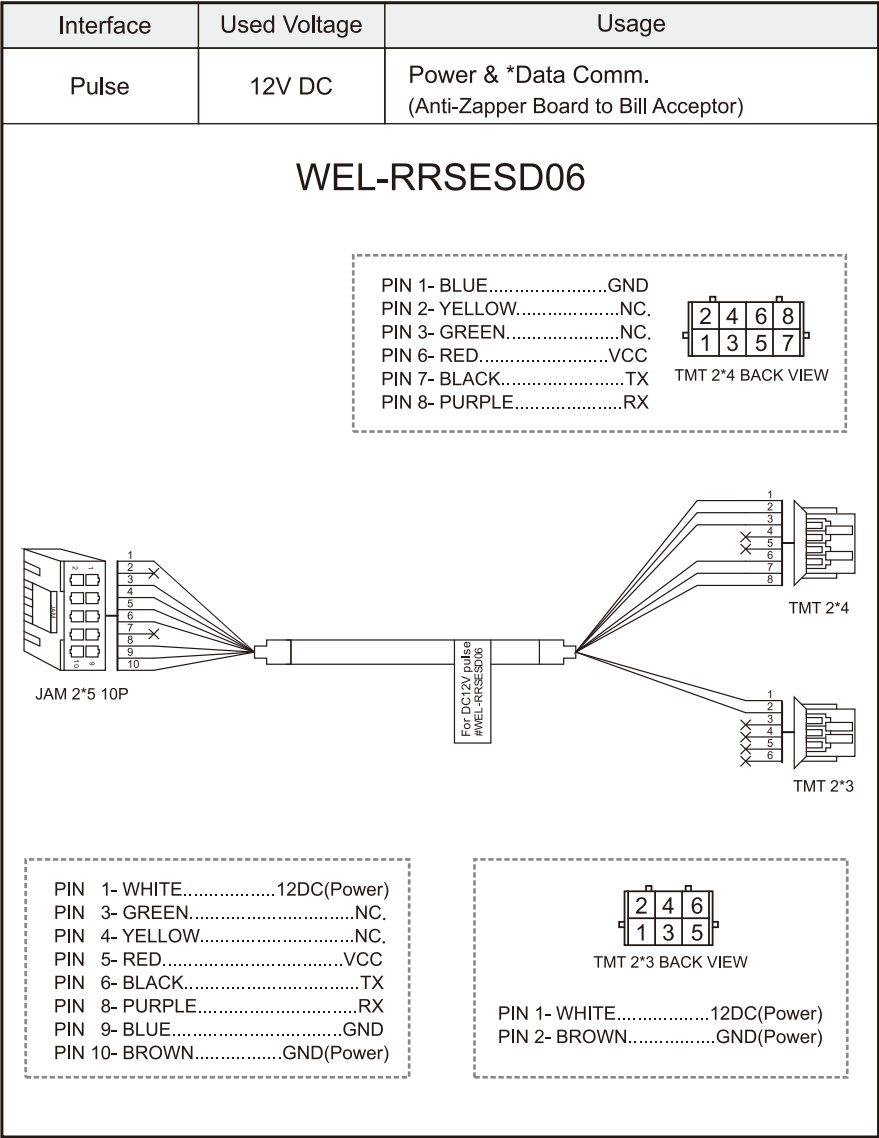
4-1 FIG.03



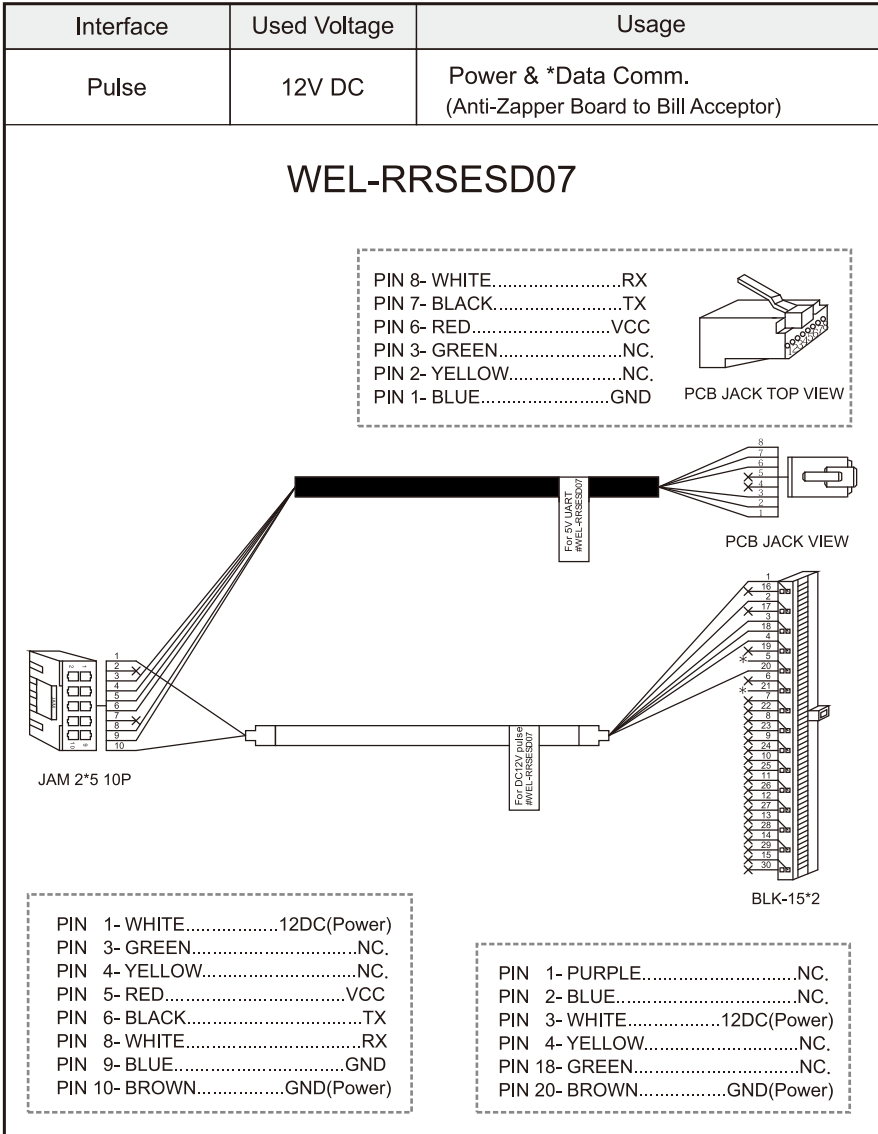


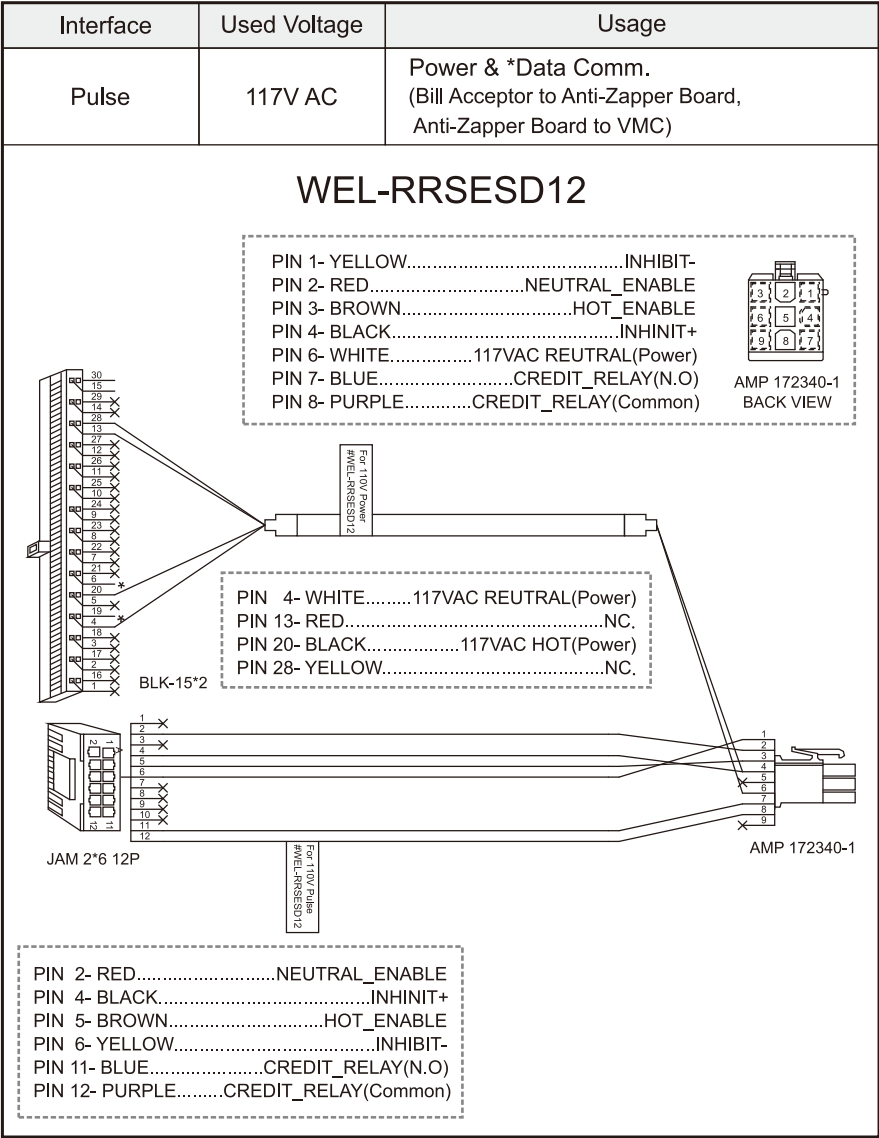
4-1 FIG.05



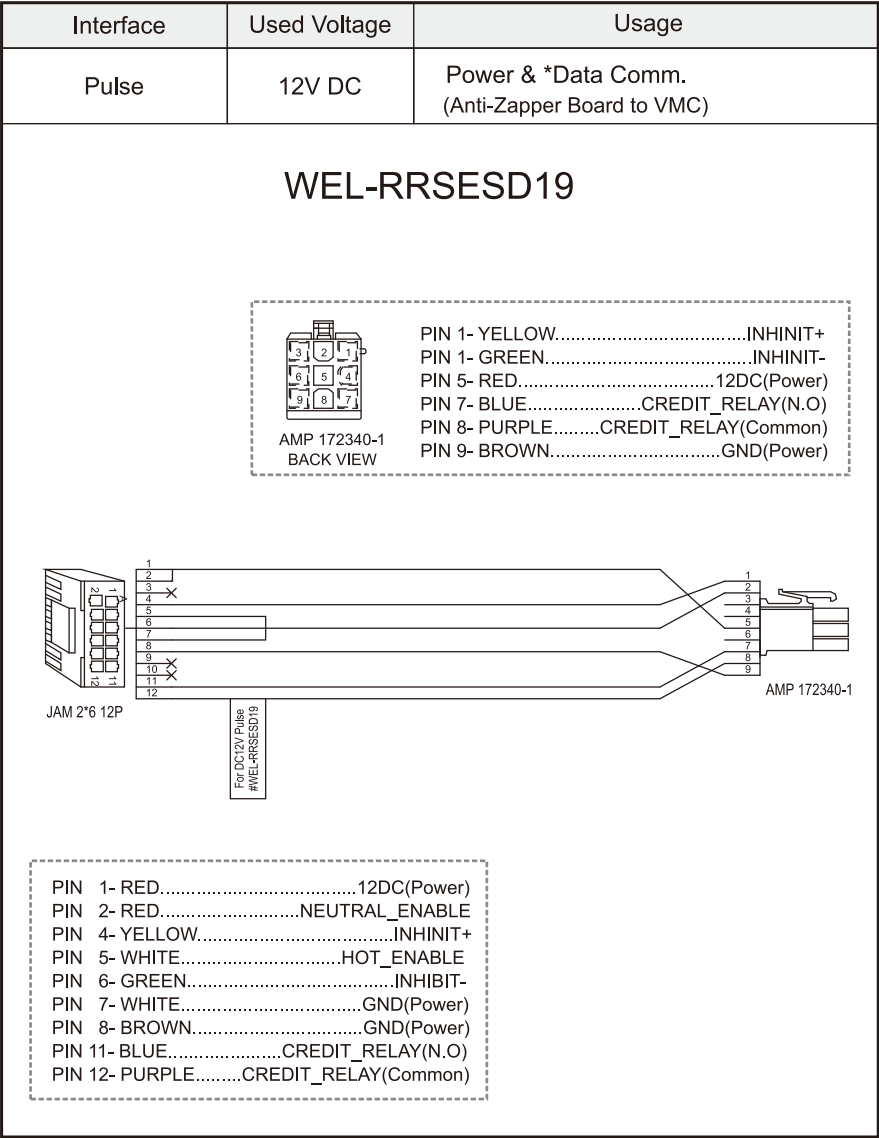


4-1 FIG.07

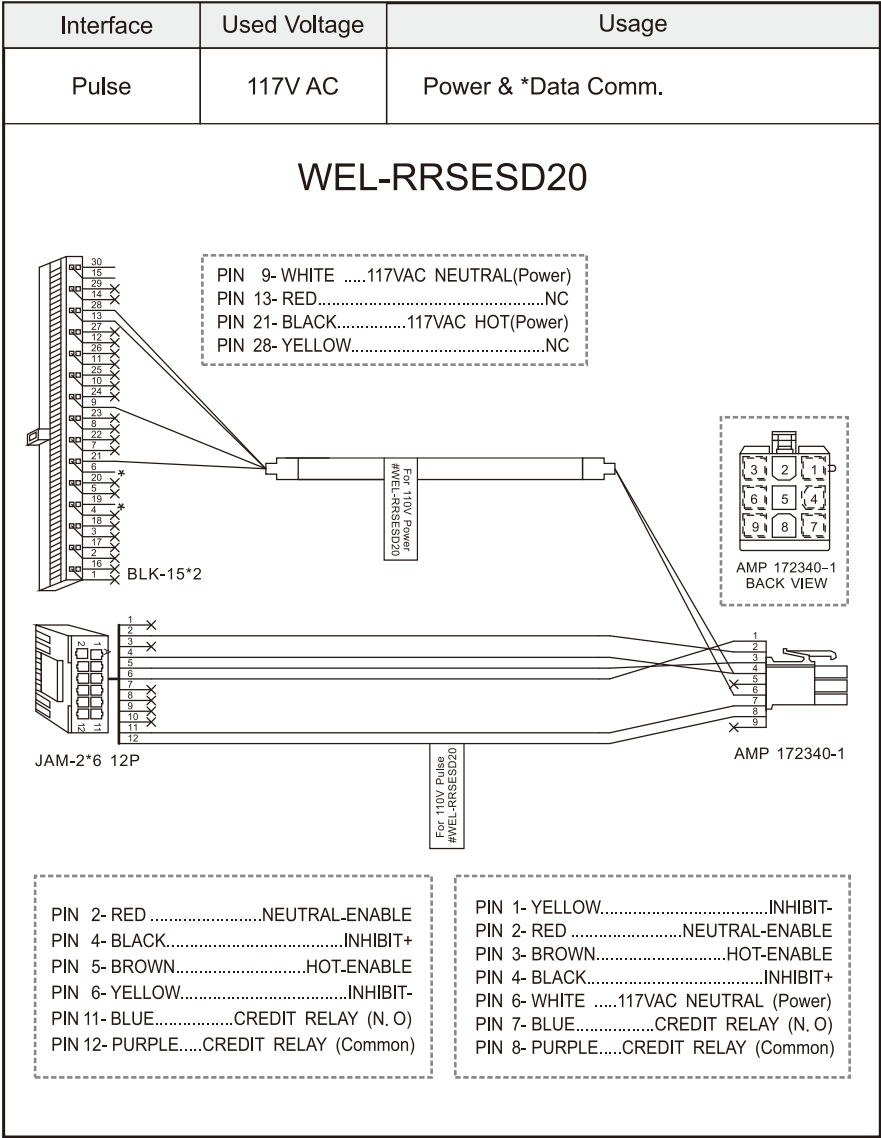




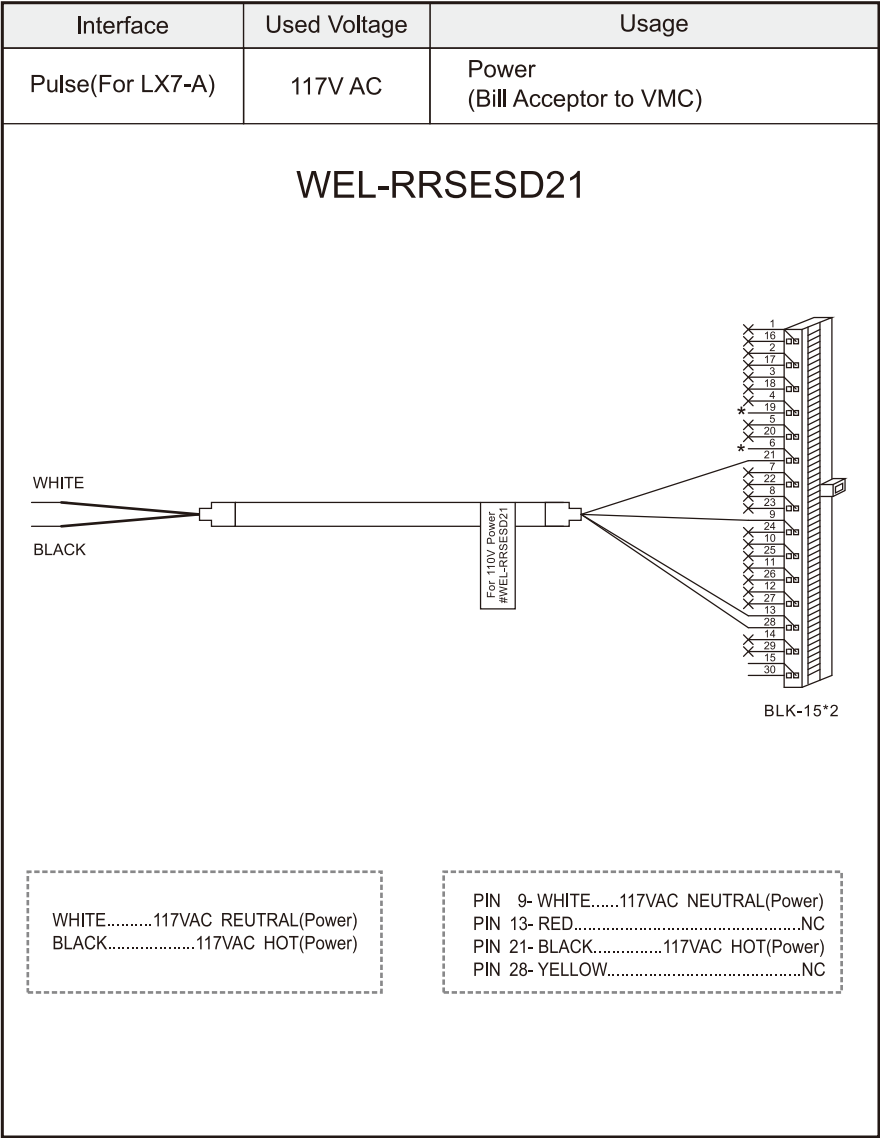
4-1 FIG.09



4-1 FIG.10



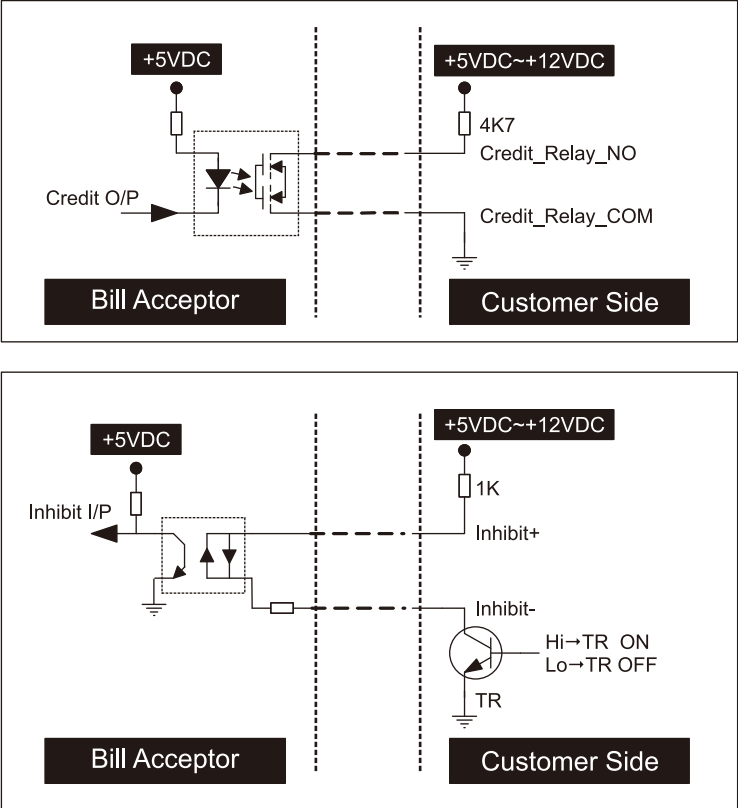
4-1 FIG.11



4-1-1. I/O Circuit

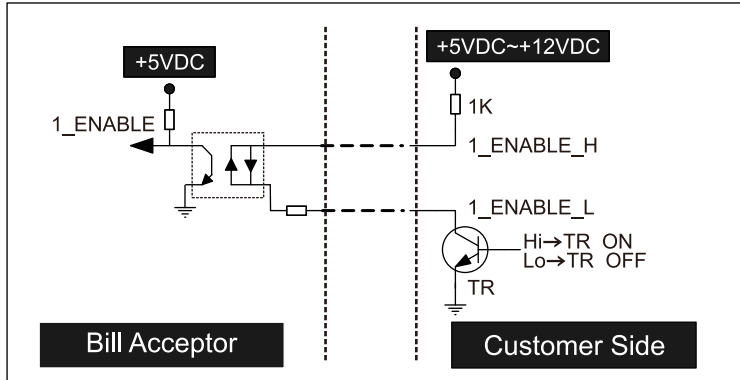
Pulse Interface.

4-1-1 FIG.01



5V Enable Interface

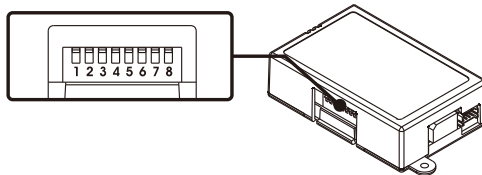
4-1-1 FIG. 02



4-2. DIP Switch Setting

There's a serial DIP switches which is located on the side of Anti-Zapper Board. According to different currencies or interfaces which are used by users, DIP switch settings could be varied to fit users' need.

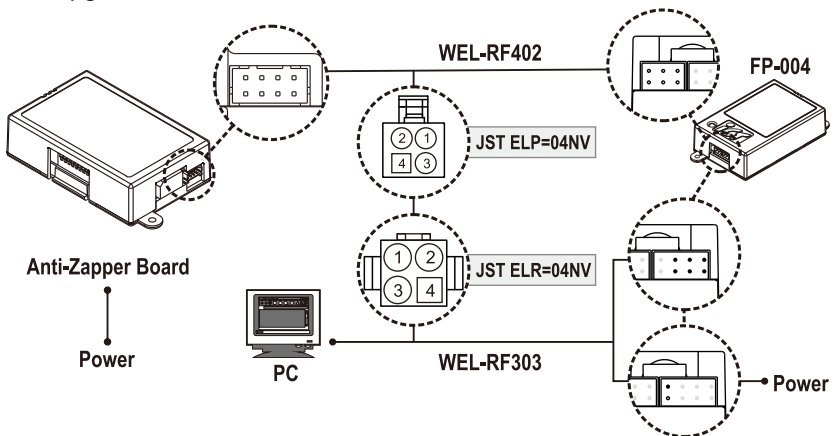
Please refer to "Anti-Zapper Board DIP Switch Setting" Guide in the package for more detail.



4-2 FIG.01

4-3. Software Download and Upgrade

To download and upgrade the software to Anti-Zapper Board, the programmer (FP-004) is needed. Please contact ICT to purchase FP-004 and refer to FP-004 user guide for software download and upgrade information.



Power must be applied to Anti-Zapper Board **after** connecting.



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